

WANG, et al.

Serial No. 09/182,626

REMARKS

Before this response, claims 15-56 were pending in the application. Applicants have cancelled claims 15-56 and added new claims 57-61. Claims 57-61 are now pending. Applicants have amended FIG. 6 to correct a typographical error and have attached a substitute drawing sheet including the corrected FIG. 6.

In the office action dated September 15, 2004, the Examiner objected to the drawings under 37 CFR 1.83(a) "because they failed to show when the output of the timer 64 is in the low state, ...the pin contact 24 maintains a high voltage state ...provides backlighting the keypad 42." Applicants respectfully submit that the FIG. 5 complies with 37 CFR 1.83(a) and no correction is required. As recited in the specification at page 10, lines 26-29: "When the output of the timer 64 is in the low state, the PNP transistor 66 turns on and the pin contact maintains a high voltage state that activates the lighting circuit 52". As illustrated in FIG. 5, when the timer output is low, the base of the PNP transistor 66 is low, the voltage from emitter to base is positive, current flows through the transistor, and the voltage source 62 is connected to pogo pin 30 providing a power source to lighting circuit 52.

The Examiner further stated that FIG. 6 and the description in lines 25- 28 of page 11 do not agree with the described passage on page 10, lines 26-29. Applicants have corrected FIG. 6. It appears that due to a typographical error, the label "READ KEYPAD" and the label "BACKLIGHT" in FIG. 6 have been reversed. Applicants respectfully submit that after correction of FIG. 6, the sections of the specification are in agreement with each other and the figures. The specification at page 11, lines 25-28 recites: "With reference to Figs. 5 and 6, when the square wave signal 92 is in the high state 94, the key pad is read via the ADC 68 and the PDA computer 56. When the square wave signal 92 is in the low state 96, the keypad 42 is back lit via the voltage source (Vs) 62." Since the voltage at the pogo pin 30 is connected to the voltage source 62 when the output of the timer is in the low state, the lighting circuit is activated when the timer output is in the low state.

The Examiner rejected the 15, 17-20 and 56 under 35 U.S.C. 102 as being anticipated by Metroka et al. (US PAT. No. 5,175,759) ("Metroka") and rejected claims 16, 21-23, 25-27, 29-47, 49 and 51-55 under 35 U.S.C. 103 as unpatentable

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over Metroka in view of Bowen et al. (US Pat. No. 6,046,730) ("Bowen"). Applicants respectfully submit that the new claims are allowable over the art cited. Applicants submit that all of the limitations of claim 57 are not taught or suggested by Metroka, nor Bowen, nor a combination of the two.

For example, claim 57 recites a mobile communication device "comprising a multiplexing circuit configured to alternate between connecting a power supply to the keypad lighting circuit and connecting the keypad to a microprocessor through the pin contact". Applicants respectfully submit that this feature is not taught or suggested by the references cited, either individually or in combination. Metroka discusses separate electrical connections between the illuminating diodes 536 - 541 and the keypad 110' (see Fig. 5). Metroka does not discuss a multiplexing circuit that alternately connects the lighting circuit to a power source and a microprocessor to a keypad. Although the illuminating diode can be controlled in Metroka, the electrical connections to both circuits are continuous. Further, Bowen has no discussion of alternating connections as claimed.

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Conclusion

In light of the foregoing, the Examiner's reconsideration of this application with a view toward allowance is respectfully requested.

The Examiner is invited to call the undersigned agent if a telephone call could help solve any remaining items.

The Commissioner is hereby authorized to charge any additional fees that may be required, or credit any overpayment, to Deposit Account No. 17-0026.

Respectfully submitted,

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